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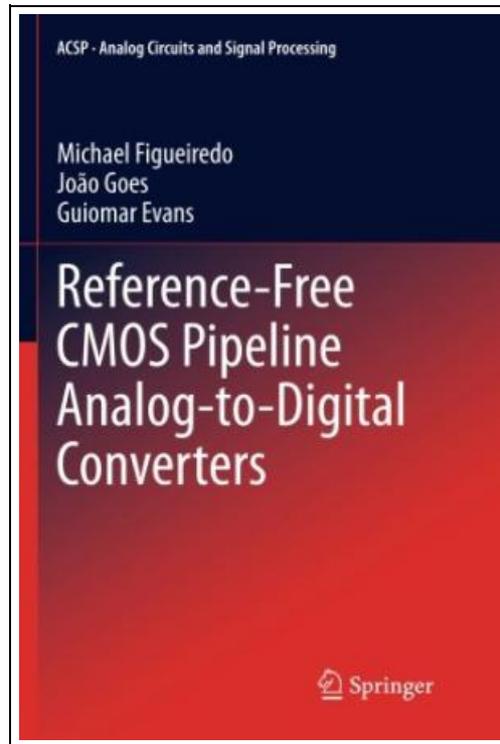Michael Figueiredo
João Goes
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Reference-Free CMOS Pipeline Analog-to-Digital Converters

Springer

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Reviews

An extremely wonderful pdf with lucid and perfect explanations. I could possibly comprehended every little thing out of this created e pdf. Once you begin to read the book, it is extremely difficult to leave it before concluding.
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Springer-Verlag New York Inc., United States, 2014. Paperback. Condition: New. 2013 ed.. Language: English . Brand New Book ***** Print on Demand *****.This book shows that digitally assisted analog to digital converters are not the only way to cope with poor analog performance caused by technology scaling. It describes various analog design techniques that enhance the area and power efficiency without employing any type of digital calibration circuitry. These techniques consist of self-biasing for PVT enhancement, inverter-based design for improved speed/power ratio, gain-of-two obtained by voltage sum instead of charge redistribution, and current-mode reference shifting instead of voltage reference shifting. Together, these techniques allow enhancing the area and power efficiency of the main building blocks of a multiplying digital-to-analog converter (MDAC) based stage, namely, the flash quantizer, the amplifier, and the switched capacitor network of the MDAC. Complementing the theoretical analyses of the various techniques, a power efficient operational transconductance amplifier is implemented and experimentally characterized. Furthermore, a medium-low resolution reference-free high-speed time-interleaved pipeline ADC employing all mentioned design techniques and circuits is presented, implemented and experimentally characterized. This ADC is said to be reference-free because it precludes any reference voltage, therefore saving power and area, as reference circuits are not necessary. Experimental results demonstrate the potential of the techniques which enabled the implementation of area and power efficient circuits.

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